



Product Description: T42	20HW04 V8	TFT-LCD PANEL				
AUO Model Name T42	0HW04 V8					
Customer Part No/Project Name:						
Customer Signature	Date	AUO	Date			
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		Ryan Chiang 2009. 8.13.				
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Date:2009/8/27

Product Functional Specification

42" Full-HD Color TFT-LCD Module Model Name: T420HW04 V8

() Preliminary Specification (*) Final Specification

Note: This specification is subject to change without notice.

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T420HW04 V8



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Record of Revision

Version	Date	Page	Old Description	New Description	Remark
0.0	2009/8/27			Final Spec	

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T420HW04 V8





1. General Description

This specification applies to the 42 inch Color TFT-LCD Module T420HW04 V8. This LCD module has a TFT active matrix type liquid crystal panel 1920x1080 pixels, and diagonal size of 42 inch. This module supports 1920x1080 Full-HD mode (Non-interlace).

Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 8-bit gray scale signal for each dot.

The T420HW04 V8 has been designed to apply the 8-bit 2 channel LVDS interface method. It is intended to support displays where high brightness, wide viewing angle, high color saturation, and high color depth are very important.

T420HW04 V8 Backlight unit used C-balance board (inverter-less) solution. This backlight unit should bundle integral TV power system to use.

* General Information

Items	Specification	Unit	Note	
Active Screen Size	42.02	inches		
Display Area	930.24(H) x 523.26(V)	mm		
Outline Dimension	983.0(H) x 576.0(V) x 44.1(D)	mm	Without inverter	
Driver Element	a-Si TFT active matrix			
Display Colors	16.7M	Colors		
Color Gamut	72	%	NTSC	
Number of Pixels	1920 x 1080	Pixel		
Pixel Pitch	0.4845	mm		
Pixel Arrangement	RGB vertical stripe			
Display Mode	Normally Black			
Lamp quantity, type	16pcs, Straight type	pcs		
Surface Treatment	НС, 3Н			





2. Absolute Maximum Ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Item	Symbol	Min	Max	Unit	Note
Power Supply Input Voltage	VDD	-0.3	14	[Volt]	1
Logic Input Voltage	Vin	-0.3	3.6	[Volt]	1
Ambient Operating Temperature	Тор	0	+50	[°C]	2
Ambient Operating Humidity	Нор	10	80	[%RH]	2
Storage Temperature	Тѕт	-20	+60	[°C]	2
Storage Humidity	Нѕт	10	80	[%RH]	2
Shock (non-operation)		-	50	G	3
Vibration (non-operation)		-	1.5	G	4
Thermal shock		-20	60	С	5
Panel surface temp			60	С	6

Note 1 : Duration = 50msec

Note 2 : Maximum Wet-Bulb should be 50 ℃ and No condensation.

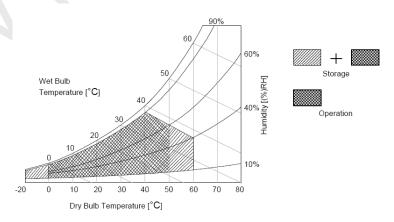
Note 3: Half sine wave, shock level: 50G(11ms), direction: ±x, ±y, ±z (one time each direction)

Note 4: Wave form: Random, vibration level: 1.5G RMS, Bandwidth: 10~3 00Hz

Duration: X,Y,Z 30min (one time each direction)

Note 5: -20C/1hr ~ 60C/1hr, 100 cycles

Note 6 :Panel only (without TV set), Ambient temp 25C



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3. Electrical Specification

The T420HW04 V8 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input, which powers the CCFL, is typically generated by an power system.

3-1 Electrical Characteristics

Parameter		Symbol		Values		Unit	Notes
			Min	Тур	Max		
LCD:							
Power St	upply Input Voltage	Vdd	10.8	12	13.2	Vdc	
Power St	upply Input Current	ldd	-	0.64	1.5	Α	1
Power Co	onsumption	Pc	-	7.68	18	Watt	1
Inrush Cu	urrent	I _{RUSH}	-	-	4	Α	4
LVDS	Differential Input	VTH			+100	mV	
Interface	High Threshold		< (3
	Voltage						
	Differential Input	VTL	-100			mV	
	Low Threshold						3
	Voltage						
	Common Input	Vсім	0.6	1.2	1.8	V	
	Voltage						
CMOS	Input High	ViH	2.0		3.3	Vdc	
Interface	Threshold Voltage	(High)					
	Input Low	VIL	0		0.8	Vdc	
	Threshold Voltage	(Low)					
Life Time			50000	-		Hours	2

The performance of the Lamp in LCM, for example life time or brightness, is extremely influenced by the characteristics of the Integrated Power Board (IPB). All the parameters of an IPB should be carefully designed so as not to produce too much leakage current from high-voltage output of the IPB. When you design or order the IPB, please make sure unwanted lighting caused by the mismatch of the lamp and the IPB (no lighting, flicker, etc) never occurs. When you confirm it, the LCD Assembly should be operated in the same condition as installed in your instrument.

Do not attach a conducting tape to lamp connecting wire. If the lamp wire attach to conducting tape, TFT-LCD Module have a low luminance and the inverter has abnormal action because

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leakage current occurs between lamp wire and conducting tape.

The relative humidity must not exceed 80% non-condensing at temperatures of 40 °C or less. At temperatures greater than 40 $^\circ\!\mathbb{C}$, the wet bulb temperature must not exceed 39 $^\circ\!\mathbb{C}$. When operate at low temperatures, the brightness of CCFL will drop and the lifetime of CCFL will be reduced.

Note:

- 1. Vdd=12.0V, fv=60Hz, fcLk=80 Mhz , 25 $^{\circ}$ C, Vdd Duration time= 470 μs , Test pattern : white pattern
- 2. The life is determined as the time at which luminance of the lamp is 50% compared to that of initial value at the typical lamp current on condition of continuous operating at 25 ±2°C.
- 3. VCIM = 1.2V

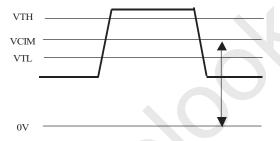
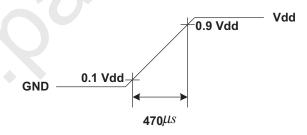


Figure: LVDS Differential Voltage

4. Measurement Condition: Rising time = 470 μ s







3-2 Interface Connections

- LCD connector: P-TWO 187059-5122 which is compatible FI-RE51S-HF (JAE)

No	Symbol	Description	No	Symbol	Description
1	GND	Ground	27	GND	Ground
2	NC	No connection	28	RE0N	SECOND CHANNEL 0-
3	NC	No connection	29	RE0P	SECOND CHANNEL 0+
4	NC	No connection	30	RE1N	SECOND CHANNEL 1-
5	NC	No connection	31	RE1P	SECOND CHANNEL 1+
6	Reserved		32	RE 2N	SECOND CHANNEL 2-
		LVDS Option			
7	LVDS SEL	Open/Low : NS mode	33	RE 2P	SECOND CHANNEL 2+
		High: JEIDA mode			
8	NC	No connection	34	GND	Ground
9	Reserved		35	RECLKN	SECOND CLOCK CHANNEL C-
10	Reserved		36	RECLKP	SECOND CLOCK CHANNEL C+
11	GND	Ground	37	GND	Ground
12	RO 0N	FIRST CHANNEL 0-	38	RE3N	SECOND CHANNEL 3-
13	RO 0P	FIRST CHANNEL 0+	39	RE3P	SECOND CHANNEL 3+
14	RO 1N	FIRST CHANNEL 1-	40	NC	No connection
15	RO 1P	FIRST CHANNEL 1+	41	NC	No connection
16	RO 2N	FIRST CHANNEL 2-	42	GND	Ground
17	RO 2P	FIRST CHANNEL 2+	43	GND	Ground
18	GND	Ground	44	GND	Ground
19	ROCLKN	FIRST CLOCK CHANNEL C-	45	GND	Ground
20	ROCLKP	FIRST CLOCK CHANNEL C+	46	GND	Ground
21	GND	Ground	47	NC	No connection
22	RO 3N	FIRST CHANNEL 3-	48	VLCD	Power Supply +12V
23	RO 3P	FIRST CHANNEL 3+	49	VLCD	Power Supply +12V
24	NC	No connection	50	VLCD	Power Supply +12V
25	NC	No connection	51	VLCD	Power Supply +12V
26	GND	Ground	-	-	-

Note: 1. All GND (ground) pin should be connected together to the LCD module's metal frame.

2. All $V_{\text{\tiny LCD}}\,$ (power input) pins should be connected.

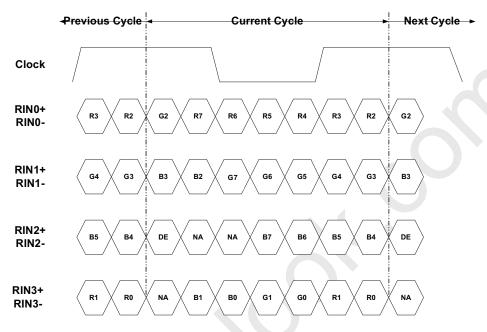
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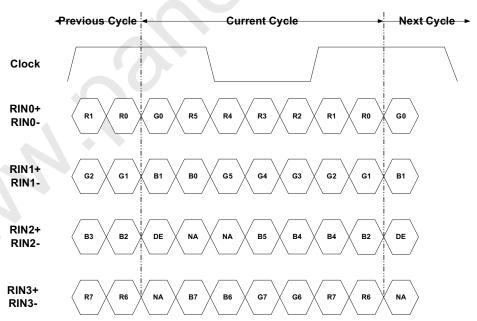




LVDS Option = High→JEIDA



LVDS Option = Low/Open→NS



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Backlight Connector Pin Configuration

Electrical specification

	Description			Min	Тур	Max	Unit	Condition
1	BL Operating Voltage		VBL	1000	1200	1400	Vrms	BL one side operating voltage at dimming ratio 100% Calculation method: (notes 1) fo=58KHz, IBL= 144mArms
2	BL Operating Current		IBL	134	144	154	mArms	BL one side operating current at dimming ratio 100%
2	Ctantina Valta na	0℃	\/-	1600	-	-	\/====	BL one side operating voltage at
3	Starting Voltage	25 ℃	Vs	1450-	-	-	Vrms	dimming ratio 100%
4	Operating frequency		fo	60	62	64	kHz	_()
5	Striking time		St	1000	-	-	msec	
6	Power Consumption		PBL	162	170	178	Watt	
7	PWM Operating Frequency		F_PWM	140	-	240	Hz	95~140Hz might cause waterfall noise but not influence panel function
8	PWM Dimming Duty ratio		D_PWM	10		100	%	1. luminance is from 10% to 100% 2. note 2
9	Lamp type			St	Straight type			
10	Number of lamps			16		pcs		
11	Type of current balance				Capacito	r		
12	C ballast		Cb	21	22	23	pF	

(Ta= $25\pm5^{\circ}$ C, Turn on for 45minutes

2. Lamp specification (Recommendation)

	Description		Min	Тур	Max	Unit
1	Lamp Voltage	Vlamp		1163		Vrms
2	Lamp Current	llamp	-	9		mArms
3	Lamp frequency	flamp	40	-	80	KHz
	Starting Voltage	0 °C		-	2100	Vrms
4		25 ℃			1750	Vrms
5	Striking time	St	1000	-	-	msec
6	Discharge Stabilization Time		-	-	3	Min
7	Life time		50K	-	-	hr

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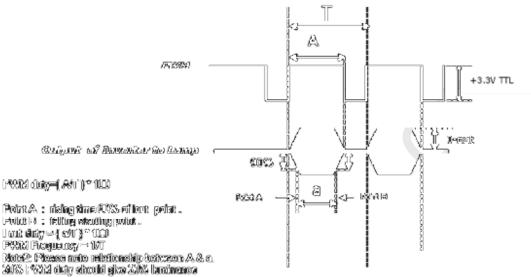
^{*} Note: At < 20% dimming ratio, AUO would not guarantee display performance & start at High and Low Temperature condition.

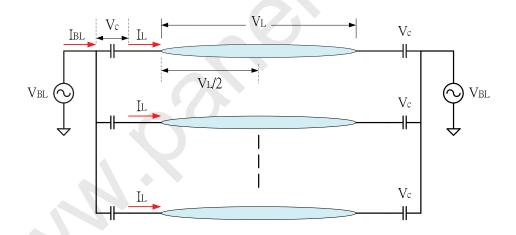


Notes 1:

$$V_{BL} = \sqrt{\left(\frac{V_L}{2}\right)^2 + \left(V_C\right)^2}$$

Notes 2:





3. Pin assignment, connector drawing and connection configuration

CN1: CI0603P1H0J-NH (CviLux) which is compatible with B03B PASK-1 (JST)

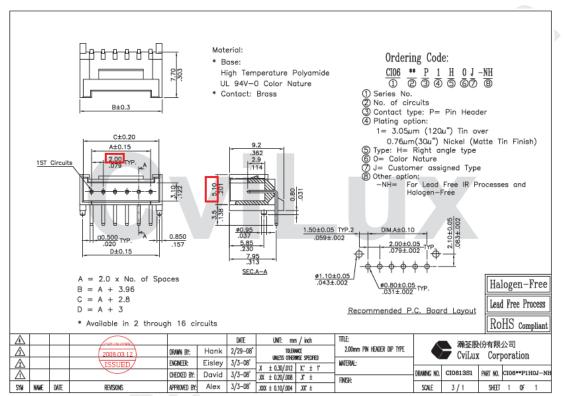
		<u> </u>
PIN#	Symbol	Description
1	High	I/P board high voltage supply
2	N.C.	No connection
3	High	I/P board high voltage supply



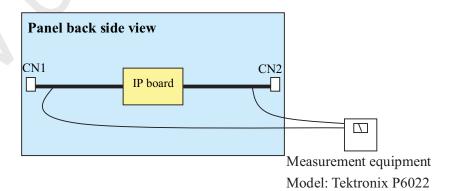


CN2: CI0603P1H0J-NH (CviLux) which is compatible with B03B PASK-1 (JST)

PIN#	Symbol	Description			
1	High	I/P board high voltage supply			
2	N.C.	No connection			
3	High	I/P board high voltage supply			



4. Measurement method



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3-3 Signal Timing Specifications

This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications for it's proper operation.

Timing Table (DE only Mode)

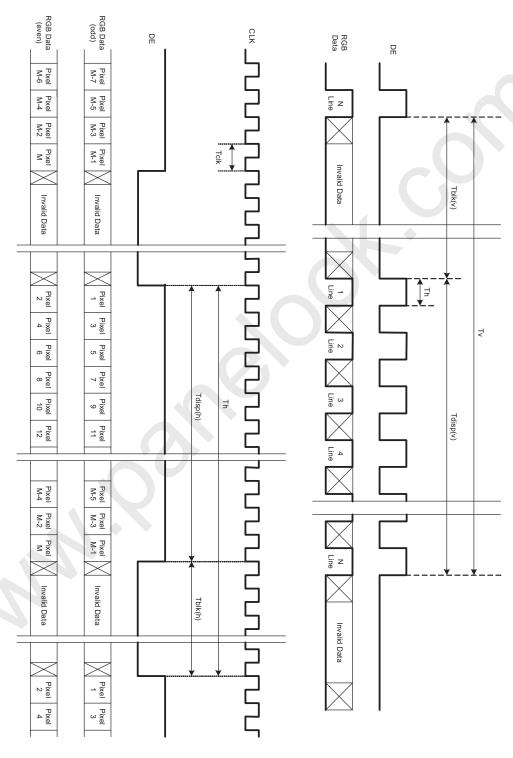
Vertical Frequency Range

Signal	Item	Symbol	Min	Type	Max	Unit
	Period	Tv	1090	1125	1480	Th
	Active	Tdisp (v)		1080		Th
Vertical Section	Blanking	Tblk (v)	10	45	400	Th
	Period	Th	1030	1100	1300	Tclk
	Active	Tdisp (h)		960		Tclk
Horizontal Section	Blanking	Tblk (h)	70	140	340	Tclk
Clock	Period	CLK	20	13.47	12.19	ns
Clock	Frequency	Freq	50	74.25	82	MHz
Vertical Frequency	Frequency	Vs	47	60	63	Hz
Horizontal Frequency	Frequency	Hs	60	67.5	73	KHz





3-4 Signal Timing Waveforms



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3-5 Color Input Data Reference

The brightness of each primary color (red, green and blue) is based on the 8 bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

COLOR DATA REFERENCE

												nnu	t Co	lor l	Data										
Color	Color			RED					Input Color Data GREEN				BLUE												
		MSB LSB				MS					MSB LSB														
		R7		R5	R4	R3	R2		R0	G7		G5	G4	G3	G2			B7		В5	R4	В3	B2	1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	<u> </u>	L	1	1	0	0	0	0	0	0	0	0	0	0		0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(001)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RED			İ													ļ						b			ļ
	RED(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
GREEN	1																								
	GREEN(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	BLUE(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE												 ! ! !			<u> </u>	<u> </u>									dimine es
	BLUE(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

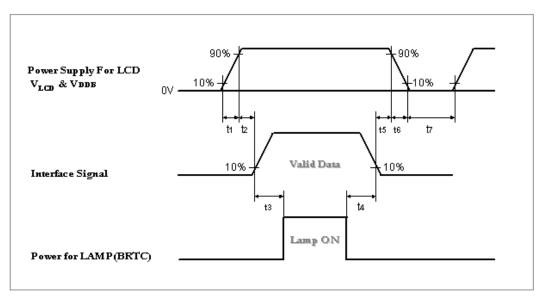
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3-6 Power Sequence

1. Power sequence of panel



		- Units		
Parameter	Min.	Тур.	Max.	Units
t1	0.4		30	ms
t2	0.1	-	50	ms
t3	300	-	-	ms
t4	10	-	-	ms
t5	0.1	-	50	ms
t6		-	300	ms
t7	500	-	-	ms

Apply the lamp voltage within the LCD operating range. When the backlight turns on before the LCD operation or the LCD turns off before the backlight turns off, the display may momentarily become abnormal.

Caution: The above on/off sequence should be applied to avoid abnormal function in the display. In case of handling, make sure to turn off the power when you plug the cable into the input connector or pull the cable out of the connector.

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4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 60 minutes in a dark environment at 25 °C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0°.

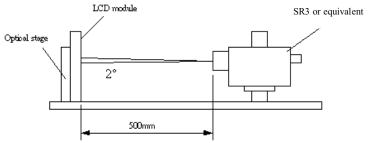


	Fig	.4-1 Optio	cal me	asuremen	t equipm	ent and me	thod	
Para	Sym	bol		Values		Units	Notes	
			Min.	Тур.	Max.			
Contrast Ratio	CR		4000	5000			1	
Surface Lumir	LWH		400	500		cd/m²	2	
Luminance Va	ariation	δ white	5р			1.3		3
Response Tim	ne (Average)	Тγ	,		6.5		ms	4,5 (Gray to Gray)
Color Coordin	ates							
	RED	R _{>}			0.640			
		R			0.330			
	GREEN	G _{>}	(=	0.290]		
		G _v	,	Typ0.03	0.600	-Typ.+0.03		
	BLUE	В	(Тур0.03	0.150	Тур.+0.03		
		В	,		0.060			
	WHITE	W;	×		0.280			
		W.	Y		0.290]		
Viewing Angle							Contrast Ratio>10	
x axis	θ	r		89		Degree	6	
x axis	θ_{\perp}			89				
y axis	y axis, up(φ =90°)				89			
y axis	y axis, down (φ =0°)				89			

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Note:

1. Contrast Ratio (CR) is defined mathematically as:

2. Surface luminance is luminance value at point 1 across the LCD surface 50cm from the surface with all pixels displaying white. From more information see Fig. 4-2. When $V \ DDB = 24V$, IDDB = 6.4A. $L_{WH} = L_{on1}$, Where L_{on1} is the luminance with all pixels displaying white at center 1 location.

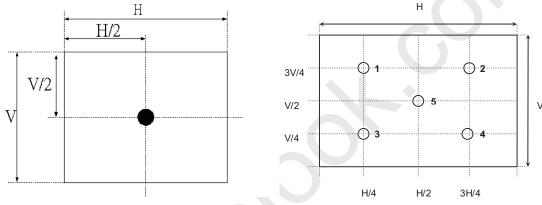


Fig.4-2 Optical measurement point

- 3. The variation in surface luminance, δ_{WHITE} is defined under 100% brightness as: $\delta_{\text{WHITE(5P)}} = \text{Maximum}(L_{\text{on1}}, L_{\text{on2}}, ..., L_{\text{on5}}) / \text{Minimum}(L_{\text{on1}}, L_{\text{on2}}, ..., L_{\text{on5}})$
- 4. Response Time:
- (a) Tr = full black to full white, 10%~90%
- (b) Tf = full white to full black, 90%~10%
- (c) G-to-G: average response time among brightness of 0%, 25%, 50%, 75% &100%.

	0%	25%	50%	75%	100%
0%		tr: 0%→25%	tr: 0%→50%	tr: 0%→75%	tr: 0%→100%
25%	tf: 25% →0%		tr: 25%→50%	tr: 25%→75%	tr: 25%→100%
50%	tf: 50% →0%	tf: 50%→25%		tr: 50%→75%	tr: 50%→100%
75%	tf: 75% →0%	tf: 75% → 25%	tf: 75%→50%		tr: 75%→100%
100%	tf: 100% → 0%	tf: 100%→25%	tf: 100%→50%	tf: 100% → 75%	

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5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see Fig. 4-3. (Optical measurement by SR3)

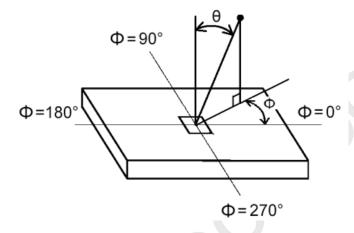


Fig.4-3 Viewing Angle Definition







5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model T420HW04 V8. In addition the figures in the next page are detailed mechanical drawing of the LCD.

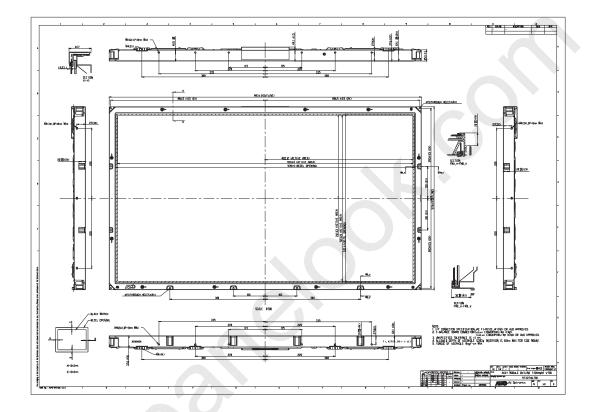
	Horizontal (typ.)	983.0mm				
Outline Dimension	Vertical (typ.)	576.0mm				
	Depth (typ.)	44.1mm (without inverter)				
Bezel Area	Horizontal (typ.)	939.0mm				
	Vertical (typ.)	531mm				
Active Display Area	Horizontal	930.24mm				
Active Display Area	Vertical	523.26mm				
Weight	12600g (typ.)					
Surface Treatment	HC, 3H					

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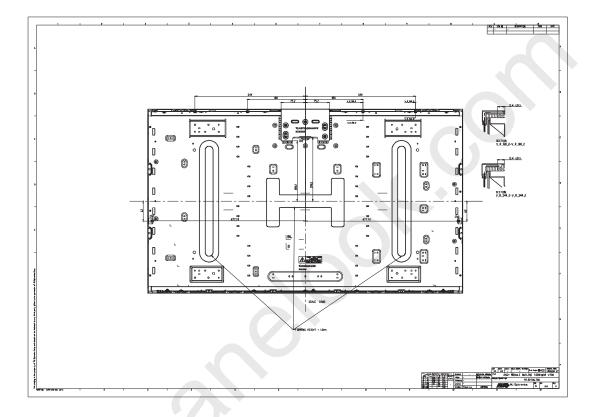
















Panel condition in RA test

Brightness: 500nits

No	Test Item	Condition
1	High temperature storage test	Ta=60 °C 300h
2	Low temperature storage test	Ta= -20 ℃ 300h
3	High temperature operation test	Ta=50 ℃ 300h
4	Low temperature operation test	Ta=-5 ℃ 300h
5	Vibration test (non-operating)	Wave form: random Vibration level: 1.5G RMS Bandwidth: 10-300Hz, Duration: X, Y, Z 30min One time each direction
6	Shock test (non-operating)	Shock level: 50G Waveform: half since wave, 11ms Direction: ±X, ±Y, ±Z One time each direction
7	Vibration test (with carton)	Wave form: random Vibration level: 1.5G RMS Bandwidth: 10-200Hz, Duration: X, Y, Z 30min One time each direction
8	Drop test (with carton)	Height: 30.5cm 1 corner, 3 edges, 6 surfaces (ASTMD4169-I)

Result Evaluation Criteria

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.





7. International Standard

7-1. Safety

- UL60065, Underwriters Laboratories, Inc. (AUO file number : E204356)
 Standard for Safety of Information Technology Equipment Including electrical Business Equipment.
- (2) CSA E60065, Canadian Standards Association Standard for Safety of Information Technology Equipment Including Electrical Business Equipment.
- (3) IEC 60065 ver. 7th, European Committee for Electro technical Standardization (CENELEC) EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business Equipment.

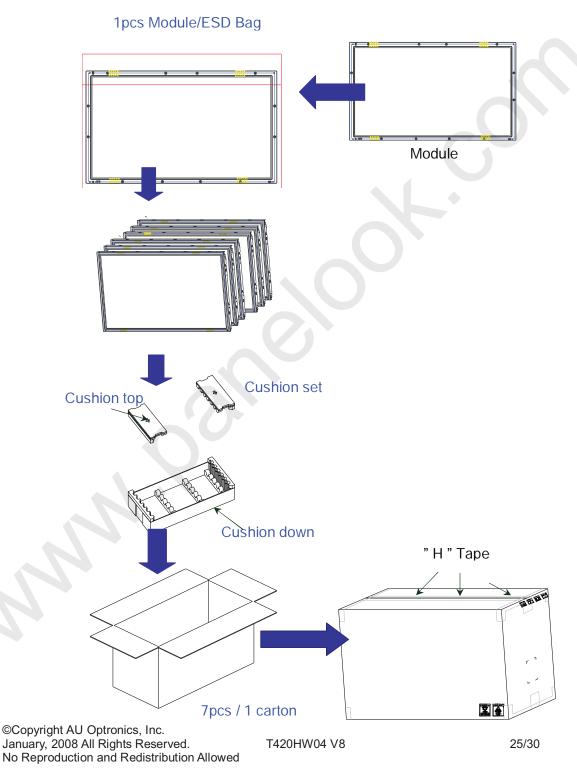
7-2. EMC

- (1) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHz to 40GHz. "American National standards Institute(ANSI), 1992
- (2) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special committee on Radio Interference.
- (3) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electro technical Standardization. (CENELEC), 1998



8.Packing

8-1 Packing Instruction







Package information:

Carton outside dimension : 1060x560x678mm

Carton/Package weight :6 kg

Shipping label



Green Mark Description:

For Pb Free products, AUO will add for identification.

For RoHS compatible products, AUO will add for identification.

Note: The Green Mark will be present only when the green documents have been ready by AUO

Internal Green Team. (The definition of green design follows the AUO green design checklist.)

Carton label



Pallet information

By air cargo::(2x1) x1 layers, one pallet put 2 boxes, 1layers(1pallet) total 14 pcs module.

By sea: (2x1) x3 layers, one pallet put 2 boxes, 3layers(3pallet) total 42 pcs module.

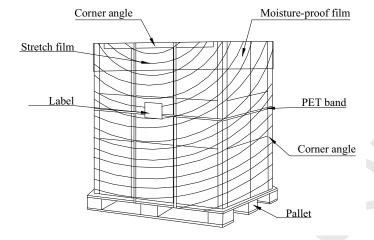
Pallet dimension: 1150x1070x132mm

Pallet weight: 10kg

By air total weight: 95kg/box X 2 boxes=190 kg (with 1 pallet weight 200kg)
By sea total weight: 95kg/box X 6 boxes=570 kg (with 3 pallet weight 600kg)







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9.PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module. $\label{eq:lcd}$

9-1 MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged on back side of panel.
- (2) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (3) You should adopt radiation structure to satisfy the temperature specification.
- (4) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (5) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (6) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front/ rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (7) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (8) Do not open the case because inside circuits do not have sufficient strength.

9-2 OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage: V=±200mV(Over and under shoot voltage)
- (2) Response time depends on the temperature. (In lower temperature, it becomes longer..)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time (required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference

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shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interface.

9-3 ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4 PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5 STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5° and 35° at normal humidity.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

9-6 HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of flue still on the Bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the Bezel or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.



Appendix

Impedance of Pin7 of LVDS : 4.2K(Ω)

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